NATIONAL CONFERENCE ON ADVANCEMENTS IN COMPUTER APPLICATIONS (NCACA2011)
December 15 - 16, 2011
REGISTRATION FORM

1. Name :...

2. Tick on the appropriate category :
   ☐ Academic institutions
   ☐ Research Scholars
   ☐ Industry or R&D organization
   ☐ P.G Students

3. Designation and Organization
   ....
   ....
   ....

4. Address for Correspondence
   ....
   ....
   ....
   ....
   ....

5. Telephone
   Office..
   Mob ....

6. Email ....

7. Presenting a paper: Yes / No
   If yes, mention Paper Id: ....

8. Accommodation Needed: Yes / No
   Visit the conference site for details

9. Food: Veg/ Non-Veg

10. Payment Details
    Amount :..
    Bank : ....
    Cash/DD NO : ....
    Dated : ....

Date :   Signature

Xerox copies of registration form are accepted

ORGANIZING COMMITTEE
Chief Patron :   Mr. P.V.Mathew
                 Chairman, Governing Body - FISAT
Patrons :   Mr. P. D. Bousally
                 Vice Chairman, Governing Body - FISAT
                 Mr. E.K. Rajavarma, Treasurer, FBOAES
Executive Chair : Dr. K. V. Sundaresan, Principal, FISAT
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                 Dr.C. Sheela, Vice Principal, FISAT
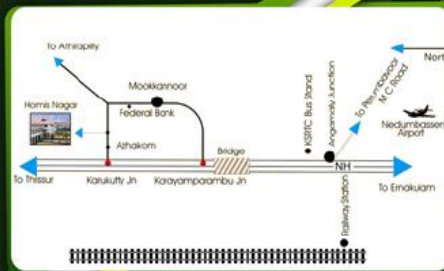Convener :   Mr. Praveen S, HOD, MCA Department, FISAT

ADVISORY COMMITTEE
1. Dr. R.Vijayakumar
   Director, School of Computer Sciences, MG University
2. Dr.P. Sojan Lal
   Professor, MBITS, Kothamangalam
3. Dr.L.Sunil Chandran
   Associate Professor, IISc, Bangalore
4. Dr.Anoop M.Namboodiri
   Assistant Professor, IIIT Hyderabad
5. Dr. G. A Vijayalakshmi Pai
   Associate Professor, PSG Tech. Coimbatore

STEERING COMMITTEE
Mr. Santhosh Kottam, MCA Department, FISAT
Ms. Deepa Mary Mathews, MCA Department, FISAT
Ms. Senu Abi, MCA Department, FISAT
Ms. Hitha Paulson, MCA Department, FISAT
Mr. Jiby James, MCA Department, FISAT
Ms. Manju Joy, MCA Department, FISAT
Ms. Nisha P John, MCA Department, FISAT
Ms. Shahna K U, MCA Department, FISAT

ROUTE MAP

NATIONAL CONFERENCE ON ADVANCEMENTS IN COMPUTER APPLICATIONS
NCACA2011
December 15 - 16, 2011

Federal Institute of Science And Technology (FISAT)™
Hormis Nagar, Mookkannoor P.O, Angamaly - 683 577
Ernakulam Dt., Kerala, India.

ABOUT THE INSTITUTION
Federal Institute of Science And Technology (FISAT)™ is a private Self Financing Engineering College, established in 2002, with the motto "Focus on Excellence" and the campus is christened as Hormis Nagar. It is an ISO 9001:2008 certified institution, run by the Federal Bank Officers' Association Educational Society (FBOAES), an initiative of Federal Bank Officers' Association (FBOA). FISAT has embarked on an ambitious plan to enrich the quality and value of education and groom individuals to high quality professionals with social commitment.

FISAT is approved by AICTE and is affiliated to Mahatma Gandhi University, Kerala, offering 6 B.Tech programmes, 3 M.Tech programmes, in addition to MCA and MBA.

The Placement and Training Cell of FISAT has succeeded in placing 90% of the eligible students in over 75 MNCs. The college is accredited by the Tata Consultancy Services (TCS) for placement activities. Student branches of IEEE, ISTE, CSI, and ISA are actively functioning in the college and have secured many national awards.

Indigenously developed 'Centre for High Performance Computing' (CHPC) research lab is utilized for projects pertaining to technical, managerial and social environments, for its implementation, simulation and performance analysis.

ABOUT THE DEPARTMENT
The MCA programme was started in 2006. The Department has a competent and committed faculty team. Regular industrial visits and personality development programmes are conducted as part of the academic programme. Consistent high results in university examination including the First Rank in 2010 MCA examination are major achievements of the department. Many reputed MNCs have recruited our graduates in the placement drives.

OBJECTIVE OF THE CONFERENCE
The goal of NCACA2011 is to provide a platform for researchers, academia and industry, to share their ideas, problems and findings related to various emerging research areas in Computer Science and Applications.

PROGRAMME SCHEDULE
Day 1: December 15, 2011, Thursday
09.00 am – Registration
09.30 am – Inaugural Function
11.00 am – Technical Session 1
            Dr. Ravindra N Guravannavar, IIT Hyderabad.
            Theme - "Research Trends in Performance Optimization of Database Applications"
12:00 pm – Paper Presentations
01:00 pm – Lunch
01.45 pm – Technical Session 2
            Dr. Naveen Sivadasan, IIT Hyderabad.
            Theme - "Data Stream Algorithms and Applications"
03:00 pm – Paper Presentations

Day 2: December 16, 2011, Friday
09.30 am – Technical Session 3
            Dr. N.K.Sakthivel, SASTRA University, Tamilnadu.
            Theme - "HPC and Data Mining Techniques for Engineering Applications"
10.45 am – Paper Presentations
01:00 pm – Lunch
01.45 pm – Technical Session 4
            Dr. G.Raju, Kannur University, Kerala.
            Theme - "Fuzzy based Image Enhancement Techniques"
03.00pm – Paper presentations
04.00pm – Valedictory function

IMPORTANT DATES
Submission of Full paper: November 12, 2011
Notification of Acceptance: November 21, 2011
Last date of registration: December 2, 2011
Conference Dates: December 15-16, 2011

REGISTRATION FEE
Industry/R&D organizations - ₹ 1500   Academic Institutions - ₹ 1000
Research Scholars   - ₹ 750   P.G Students   - ₹ 500

All payments should be made through Account Payee Demand Draft in favour of "The Principal, Federal Institute of Science And Technology" payable at Ernakulam. The filled in registration form along with the DD is to be send to the communication address cited.

TOPICS
The contributions should be original and should not have been published earlier, accepted for publication, or presently be under consideration for publication. The accepted papers will be published in the conference proceedings only after registration and presentation by any one of the authors. The topics are listed below (but not limited to)
1. High Performance Computing
2. Data Warehousing and Mining
3. Information Security
4. Cryptography and Network Security
5. Green Computing
6. Image Processing
7. Internet and Web Technology
8. Cloud Computing
9. Grid Computing
10. Nano Computing
11. Parallel and Distributed Computing
12. Web Mining
13. Fuzzy Technologies and Systems
14. Wireless and Mobile Computing
15. Multimedia and its applications
16. Multicore processing
17. M2M Communications
18. Pattern Recognition
19. Natural Language processing
20. Fault Tolerant Computing

SUBMISSION GUIDELINES
Scholastic papers are subjected to peer evaluation. Authors are requested to submit full paper in IEEE format (MS Word format), limited to maximum 5 pages, including references. The IEEE template can be downloaded from the site www.fisat.ac.in The manuscript can be mailed to ncaca2011@gmail.com.

ADDRESS FOR COMMUNICATION
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